

Figure 4b

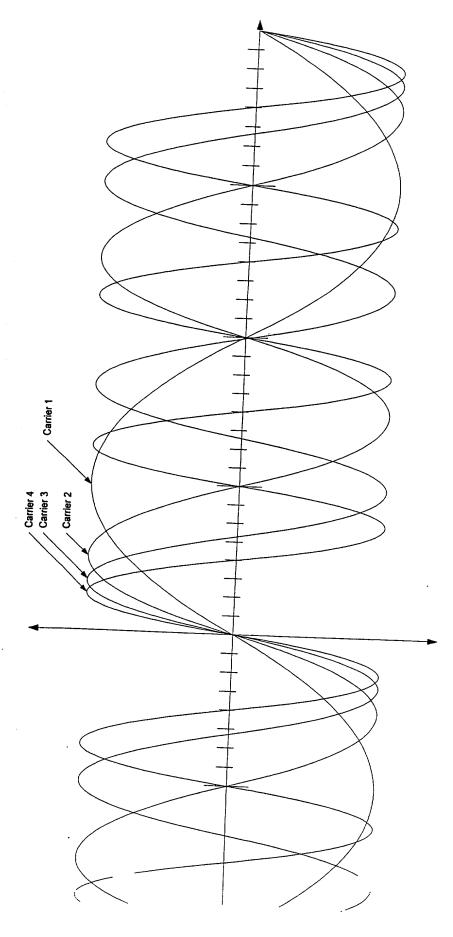


Figure 4c

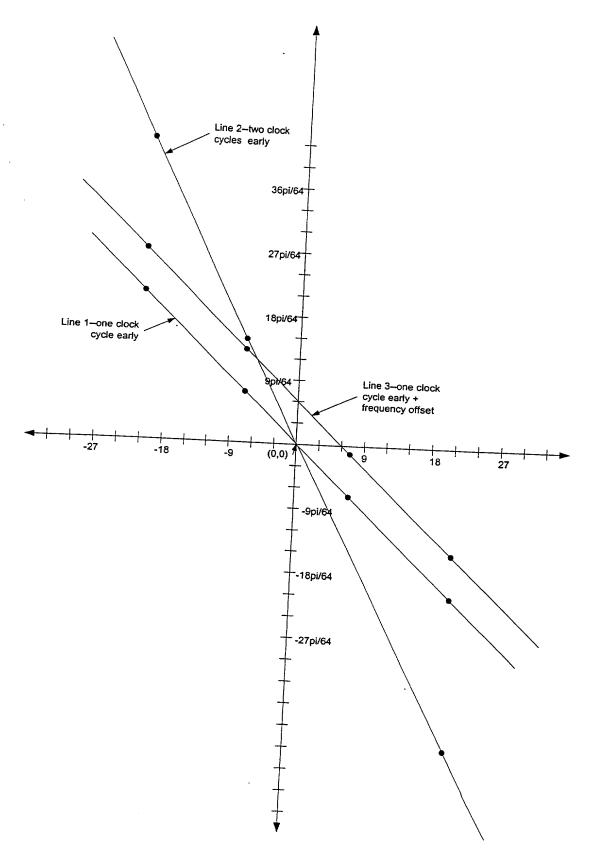
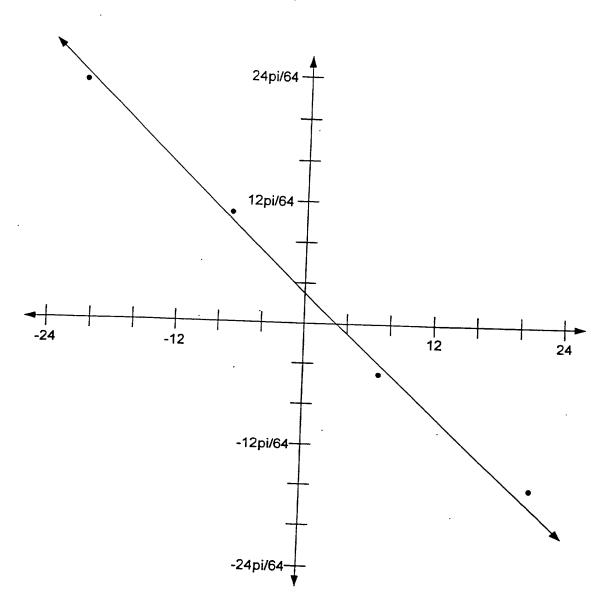


Figure 4d



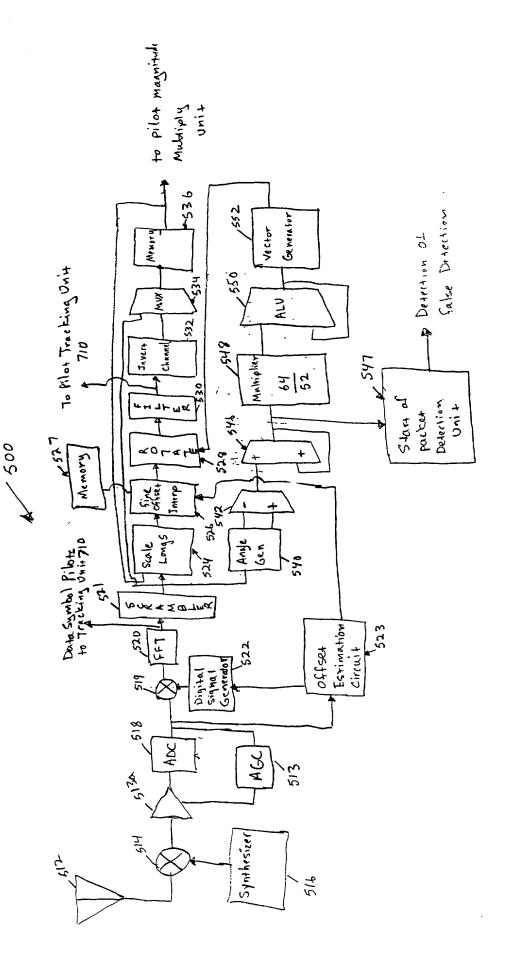


Figure 5

offset circuit

Figure

Figure 6a

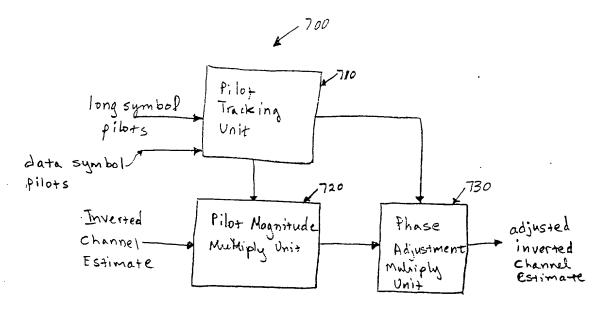


Figure 7